

Modus User Guide

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Cadence Modus DFT Software Solution

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Patent Information For a listing of Snap-on products that are protected by patents in the United States and elsewhere, visit:

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ModusToolbox™ User Guide, Document

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Number: 002-29893 Rev. *C 6 1

Introduction 1.1 What is ModusToolbox?

The ModusToolbox software environment is a set of reference flows and products that provide an immersive development experience for creating converged MCU and wireless systems. Cypress provides a set of multi-platform development tools and

when appropriate, and any changes will be set out on the ...

Modus ATPG is a comprehensive low-power test solution starting with CPF and IEEE 1801 power-intent-driven DFT insertion in the Genus cockpit, coupled with Q-pin and clock gating to control test mode power.

Cadence Modus DFT Software Solution

ModusToolbox™ IDE User Guide,
Document Number: 002-24375 Rev *A
10. Select Kit. The ModusToolbox IDE Application dialog displays a list of kits, showing the Kit Name, MCU, and

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Connectivity Device (if. applicable).

ModusToolbox IDE User Guide

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QUASAR™ Penta-Guide Phantom - Modus QA

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The Cadence Modus test solution is a comprehensive next-generation physically aware design-for-test (DFT), ATPG and silicon diagnostics tool. Using the Modus test solution, customers can experience up to 3X reduction in test time using its patented physically aware 2D Elastic Compression architecture, without any impact on fault coverage or chip size.

Cadence Modus test solution supports ARM MBIST interface ...

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